

# Download Sentaurus Process User Guide

Fig. 4 shows the I-V characteristics for a 3D NAND cell with a different channel hole etch angle in the 12 WL stack structure. The 90° etch angle cell exhibited a slightly better on-current value of  $6.43e-7$  A and lower threshold voltage value of 0.19 V compared to that of a cell with a slight tilt away from the vertical etch angle. OptSim Circuit is a design automation tool for PIC design. It is an ideal platform for modeling optical systems and photonic integrated circuits that operate with coupling and feedback of different optical and electrical signal paths.